Appendix A: List of Worked Examples

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Suggested Readings

1. Wakerly JF (2006) Digital design: principles and practice, 4th edn. Pearson Education, Upper Saddle River, NJ
2. Kang S, Leblebici Y, Kim C (2015) “Combinational MOS logic circuits” in CMOS digital integrated circuits: analysis and design, 4th edn. McGraw-Hill Education, New York, NY
3. Cady FM (2007) Software and hardware engineering. Oxford University Press, Upper Saddle River, NJ
4. Ciletti MD (2003) Modeling, synthesis, and rapid prototyping with Verilog HDL. Prentice Hall, Upper Saddle River, NJ
5. Mano MM, Ciletti MD (2012) Digital design – with an introduction to the Verilog HDL, 5th edn. Pearson, Upper Saddle River, NJ
6. Yalamanchili S (1997) VHDL starter’s guide. Prentice Hall, Upper Saddle River, NJ
7. IEEE Standard VHDL Language Reference Manual (2009) in IEEE Std 1076-2008 (Revision of IEEE Std 1076-2002), Jan. 26 2009

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